

IN THE UNITED STATES PATENT OFFICE

Applicants:	Eric D. Groen et al.		
Assignee:	Xilinx, Inc.		
Title:	MGT/FPGA CLOCK MANAGEMENT SYSTEM		
Serial No.:	10/660,254	File Date:	9/11/2003
Examiner:	Sophia Vlahos	Art Unit:	2611
Docket No.:	X-1359 US	Conf. No.:	5349

COMMISSIONER FOR PATENTS
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EX PARTE QUAYLE RESPONSE

Dear Examiner:

In response to the Exparte Quayle Office Action mailed from the Patent Office on January 25, 2010, please make the following amendments:

Amendments to the Claims is not being submitted because none of the claims are being amended.

Amendments to the Drawings begin on page 2 of this paper and include five replacement sheets.

Remarks begin on page 3 of this paper.

An **Appendix** including five amended figures are attached following page 3.